HPORTEK

PRODUCT DATASHEET

SYHS-VU13P-PROC

High-Performance Computing Board

High-Performance Computing Board. An FPGA-based real-time digital signal processing board for digital filtering, demodulation, high-speed optical data transfer and multi-board synchronization in research and automated testing.



Features

• High-performance FPGA with massive DSP & on-chip memory

• Multiple GTY transceivers + optical ports; high-bandwidth, low-latency

• External DDR4 for wideband data buffering

• External ref. clock & trigger; distributed processing clusters

• Ethernet and other high-speed protocols

Published technical information

Parameter	Specification / typical value
Functional Specifications	
Enable control: TX/RX enable timing control (optical trigger)	Supported
Signal generation: IQ signal processing and generation	Supported
Remote data transfer: data transfer via optical interface	Supported
Technical Specifications	
EMMC	≥8GB
E2PPROM	≥64Kb

Parameter	Specification / typical value
DDR	Up to 512 GB
QSPI Flash	≥32MB
Optical Interface Data Rate	100Gsps
Optical Interface Type	QSFP28
Number of Optical Interfaces	4 channels
PCIe Interface	PCIE3.0×16
GPIO Interface	40 (3.3V LVTTL)
General	
Power Consumption	≤50W

Prepared from the existing product catalogue. Specifications may change; typical values are not guaranteed limits. Confirm operating conditions, interfaces and ordering configuration with engineering. This document does not replace the equipment operating manual.

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https://www.hportek.com/en/product/high-speed-acquisition-board-syhs-vu13p-proc/